MPSA92 is a Preferred Device

High Voltage Transistors

PNP Silicon



• Pb-Free Packages are Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage MPSA93 MPSA92	V _{CEO}	-200 -300	Vdc
Collector – Base Voltage MPSA93 MPSA92	V _{CBO}	-200 -300	Vdc
Emitter – Base Voltage	V _{EBO}	-5.0	Vdc
Collector Current – Continuous	I _C	-500	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	625 5.0	mW mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

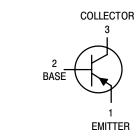
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

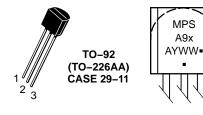


ON Semiconductor®

http://onsemi.com



MARKING DIAGRAM



= 2 or 3

A = Assembly Location

Y = Year WW = Work Week ■ = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 3 of this data sheet.

Preferred devices are recommended choices for future use and best overall value.

^{*}For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS		•			
Collector – Emitter Breakdown Voltage (Note 1) $(I_C = -1.0 \text{ mAdc}, I_B = 0)$	MPSA92 MPSA93	V _{(BR)CEO}	-300 -200	_ _	Vdc
Collector – Base Breakdown Voltage (I _C = –100 μAdc, I _E = 0)	MPSA92 MPSA93	V _(BR) CBO	-300 -200	_ _	Vdc
Emitter – Base Breakdown Voltage $(I_E = -100 \mu Adc, I_C = 0)$		V _{(BR)EBO}	-5.0	-	Vdc
Collector Cutoff Current $(V_{CB} = -200 \text{ Vdc}, I_E = 0)$ $(V_{CB} = -160 \text{ Vdc}, I_E = 0)$	MPSA92 MPSA93	I _{CBO}	_ _	-0.25 -0.25	μAdc
Emitter Cutoff Current $(V_{EB} = -3.0 \text{ Vdc}, I_C = 0)$		I _{EBO}	_	-0.1	μAdc
ON CHARACTERISTICS (Note 1)		•			
DC Current Gain ($I_C = -1.0 \text{ mAdc}$, $V_{CE} = -10 \text{ Vdc}$) ($I_C = -10 \text{ mAdc}$, $V_{CE} = -10 \text{ Vdc}$)	All Types All Types	h _{FE}	25 40	- -	_
$(I_C = -30 \text{ mAdc}, V_{CE} = -10 \text{ Vdc})$	MPSA92 MPSA93		25 25	- -	
Collector – Emitter Saturation Voltage (I _C = -20 mAdc, I _B = -2.0 mAdc)	MPSA92 MPSA93	V _{CE(sat)}		-0.5 -0.4	Vdc
Base–Emitter Saturation Voltage ($I_C = -20 \text{ mAdc}$, $I_B = -2.0 \text{ mAdc}$)		V _{BE(sat)}	_	-0.9	Vdc
SMALL-SIGNAL CHARACTERISTICS		•	•	•	
Current-Gain - Bandwidth Product (I _C = -10 mAdc, V _{CE} = -20 Vdc, f = 100 MHz)		f _T	50	_	MHz
Collector–Base Capacitance (V _{CB} = -20 Vdc, I _E = 0, f = 1.0 MHz)	MPSA92 MPSA93	C _{cb}	_ _	6.0 8.0	pF

^{1.} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.

ORDERING INFORMATION

Device	Package	Shipping [†]
MPSA92	TO-92	5000 Units / Box
MPSA92G	TO-92 (Pb-Free)	5000 Units / Box
MPSA92RL1	TO-92	2000 / Tape & Reel
MPSA92RL1G	TO-92 (Pb-Free)	2000 / Tape & Reel
MPSA92RLRA	TO-92	2000 / Tape & Reel
MPSA92RLRAG	TO-92 (Pb-Free)	2000 / Tape & Reel
MPSA92RLRM	TO-92	2000 / Ammo Pack
MPSA92RLRMG	TO-92 (Pb-Free)	2000 / Ammo Pack
MPSA92RLRP	TO-92	2000 / Ammo Pack
MPSA92RLRPG	TO-92 (Pb-Free)	2000 / Ammo Pack
MPSA92ZL1	TO-92	2000 / Ammo Pack
MPSA92ZL1G	TO-92 (Pb-Free)	2000 / Ammo Pack
MPSA93	TO-92	5000 Units / Box
MPSA93G	TO-92 (Pb-Free)	5000 Units / Box
MPSA93RLRM	TO-92	2000 / Ammo Pack
MPSA93RLRMG	TO-92 (Pb-Free)	2000 / Ammo Pack

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

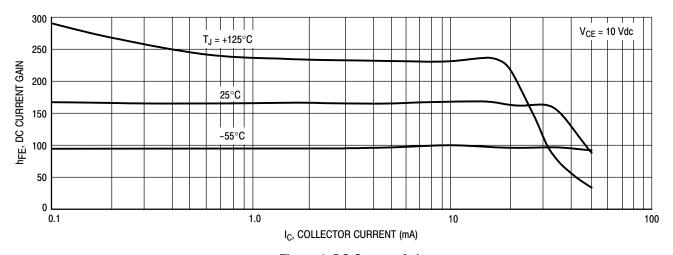


Figure 1. DC Current Gain

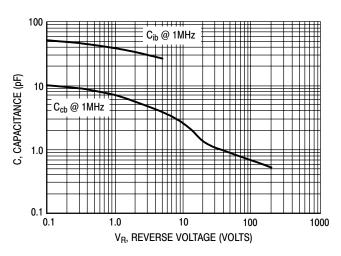


Figure 2. Capacitance

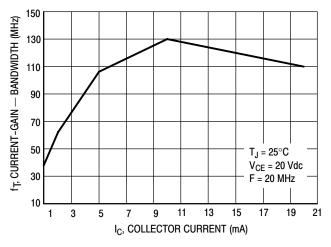


Figure 3. Current-Gain - Bandwidth

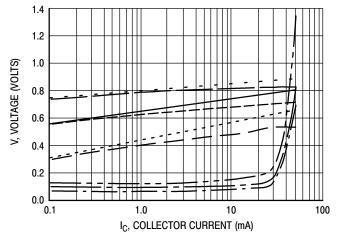
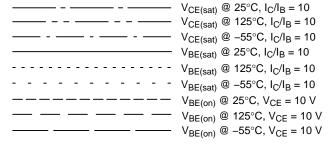
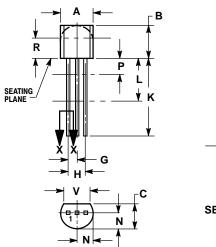


Figure 4. "ON" Voltages



PACKAGE DIMENSIONS

TO-92 **TO-226AA** CASE 29-11 **ISSUE AL**





- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
 4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.115		2.93	
٧	0.135		3.43	

STYLE 1: STYLE 14: PIN 1. EMITTER
2. BASE
3. COLLECTOR PIN 1. EMITTER
2. COLLECTOR
3. BASE

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